



**PRESS RELEASE**

## **Magillem Design Services and CEA Sign Multi-Year Collaboration Agreement**

### *Development of a Unified Hardware/Software Design Platform For Complex Systems-on-Chip*

PARIS — Feb. 21, 2011 — Magillem, the leading provider of platform solutions to cover design-flow challenges of complex systems, today announced the signing of a multi-year collaboration agreement with CEA, the French alternative energies and atomic energy commission.

The project will focus on development of unified hardware/software design tools for complex systems-on-chip (SOC) to reduce design-iteration steps and improve the verification path.

The design process of embedded systems has changed substantially in recent years. To shorten time-to-market, designers integrate more and more software to add functionality and flexibility. Current development methods for embedded systems decouple the design of application software from the design of its execution platform. This results in intractable verification of the entire system, along with sub-optimal hardware/software partitions, and discontinuities in the design flow. It also makes specifications revision difficult and directly impacts time-to-market.

Magillem brings its know-how in design methods and tools as well as innovative solutions for complex SOC design and reuse. Leti and LIST, institutes of CEA, will bring their expertise on SOC design to help Magillem extend its design technology offer. The goal is to ease hardware-and-software integration and enable global validation of SOC.

The joint-development work will take place at the CRI PILSI, the Integration Research Center of the International Software and Smart Systems Cluster, in Gières, France.

#### **About CEA-Leti**

CEA is a French research and technology organization, with activities in four main areas: energy, information technologies, healthcare technologies and defense and security. Within CEA, the Laboratory for Electronics & Information Technology (CEA-Leti) works with companies in order to increase their competitiveness through technological innovation and transfers. CEA-Leti is focused on micro and nanotechnologies and their applications, from wireless devices and systems, to biology and healthcare or photonics. Nanoelectronics and microsystems (MEMS) are at the core of its activities. As a major player in MINATEC campus, CEA-Leti operates 8,000-m<sup>2</sup> state-of-the-art clean rooms, on 24/7 mode, on 200mm and 300mm wafer standards. With 1,200 employees, CEA-Leti trains more than 190 Ph.D. students and hosts 200 assignees from partner companies. Strongly committed to the creation of value for the industry, CEA-Leti puts a strong emphasis on intellectual property and owns more than 1,700 patent families. For more information about Leti, visit [www.leti.fr](http://www.leti.fr).

### **About CEA-LIST**

CEA-LIST is a key player in Information and Communication Technologies. Its research activities are focused on Digital Systems with major societal and economic stakes: Embedded Systems, Ambient Intelligence and Information Processing. With its 650 researchers, engineers and technicians, the CEA-LIST performs innovative research in partnership with major industrial players in the fields of ICT, Energy, Transport, Security & Defence, Medical and Industrial Process. For more information, visit <http://www-list.cea.fr>.

### **About Magillem**

Magillem has developed an easy to use, state-of-the-art platform solution to cover electronic systems design flow challenges in a context where complexity, interoperability and design re-use are becoming critical issues to manage design cycle time of SOC.

**A platform software** solution to design complex systems and consolidate data by assembling existing contents (IP)

**To:** large Corporation such as:

- Texas Instruments, ST Microelectronics, Qualcomm, NXP, Sonics, ST Ericsson
- ESA, CEA, Thomson, Thales, ASTRIUM,
- Franklin, LexisNexis.

**In order to:** allow organizations which produce a large amount of data and heterogeneous documents from various sources, to reuse compounded and accumulated knowledge, providing management of valuable business content and a huge productivity gain, without disturbing existing processes.

**Products:** a set of software (Java, Xml); training programs; customization and adaptations service contracts

**Company status:** French Corporation, capital of 383 797 Euros, trading on Euronext Free market.

Company is headquartered in Paris, France, with offices in New York, USA and Tokyo, Japan. Customers include the first tier SoC manufacturers worldwide. For more information, visit <http://www.magillem.com/>

### **About CRI-PILSI**

The Integration Research Centre (CRI), established late 2009, is directed by Joseph Sifakis, 2007 Turing Award winner for his work on model checking. Its mission is to build bridges between industry and research to shorten time-to-market for innovative products, particularly smart miniaturized solutions. Its first research program aims to develop technologies for designing and programming multi-core architectures. Other projects are taking shape around smart-home and low-power/high-performance computing. CRI is part of Pilsil, the International Software and Smart Systems Cluster (Pilsil) established in Grenoble-Isere in 2009 by three national research organizations – the French National Institute for Research in Computer Science and Control (Inria), France's Atomic Energy Commission (CEA) and the National Centre for Scientific Research (CNRS) – and two universities, Grenoble-INP and Université Joseph Fourier (Grenoble-1). CRI also draws on the Minalogic competitiveness cluster, which represents industry and is supported by the city of Grenoble (La Metro). [www.ujf-grenoble.fr/1260977162566/0/fiche\\_article/](http://www.ujf-grenoble.fr/1260977162566/0/fiche_article/)