

Electronic platforms virtualization

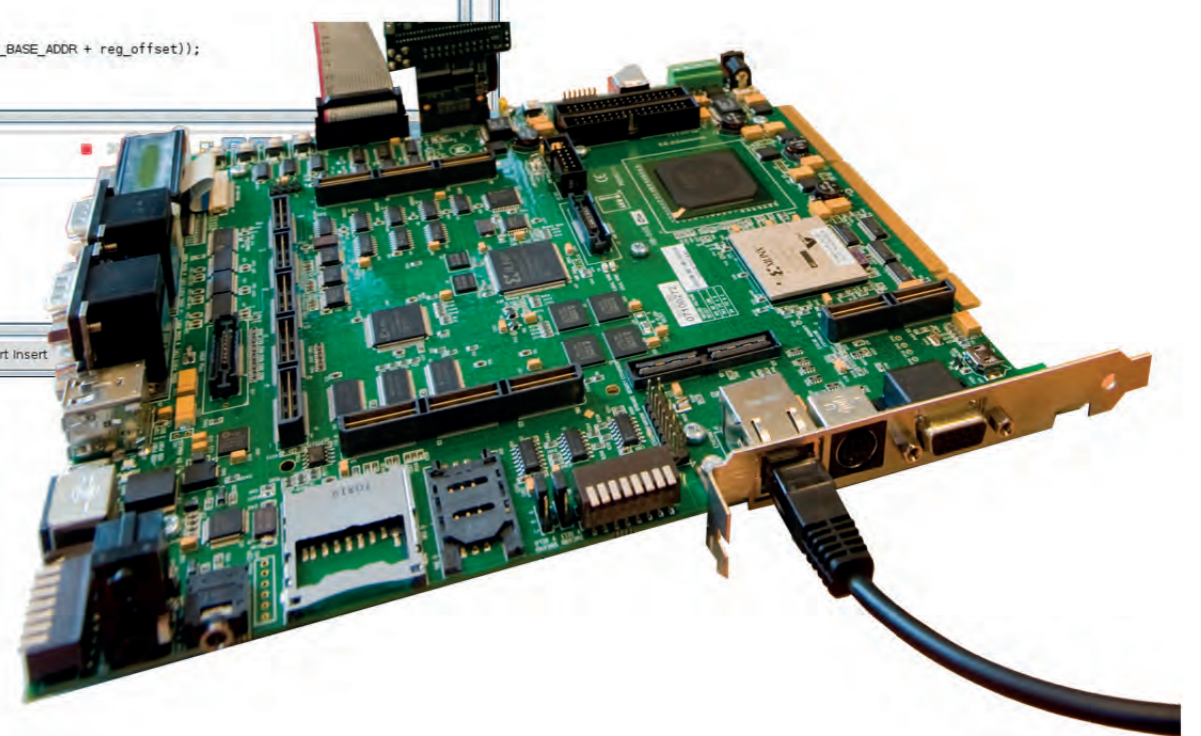
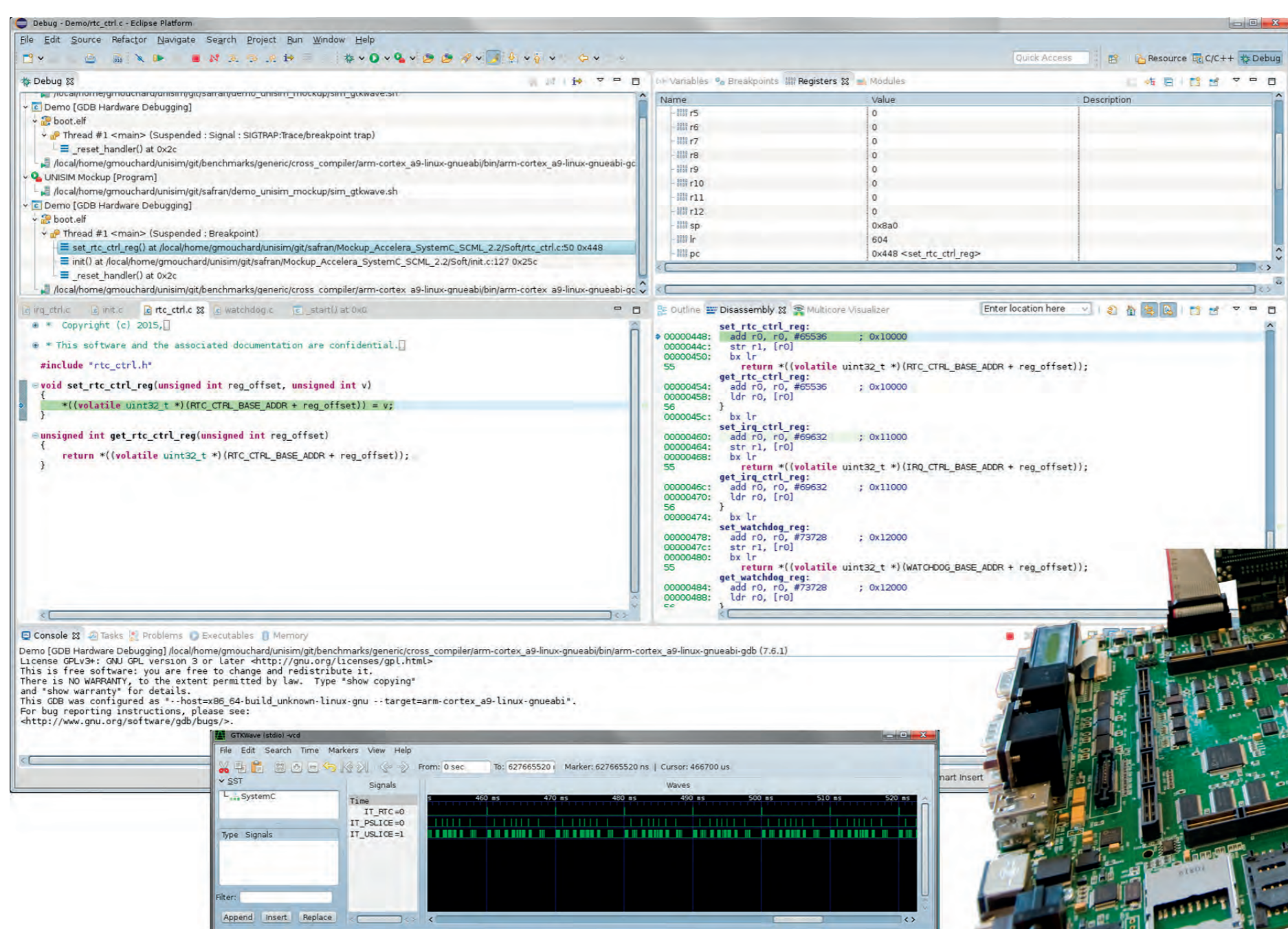
for early HW/SW integration

UNISIM-VP



A virtual prototyping solution driven by industrial requirements:

- Industrial-grade
- SystemC TLM-2.0 compliant
- Aimed for SW verification
- Open source
- Fast enough for early HW/SW integration
- Helps anticipating WCET
- Executes binary full SW stack



Assets

- Synchronization of HW and SW // development cycles
- Time-to-market reduced by 6 months
- Simplified deployment w.r.t. HW prototypes
- Early fix of HW requirements and HW/SW interface specifications
- Early SW optimization and performance testbenches
- Reduced coding errors in hardware-dependent software

Perspectives

Workbench for electronic platforms design, validation and verification (HW&SW)

Website

<http://unisim-vp.org>