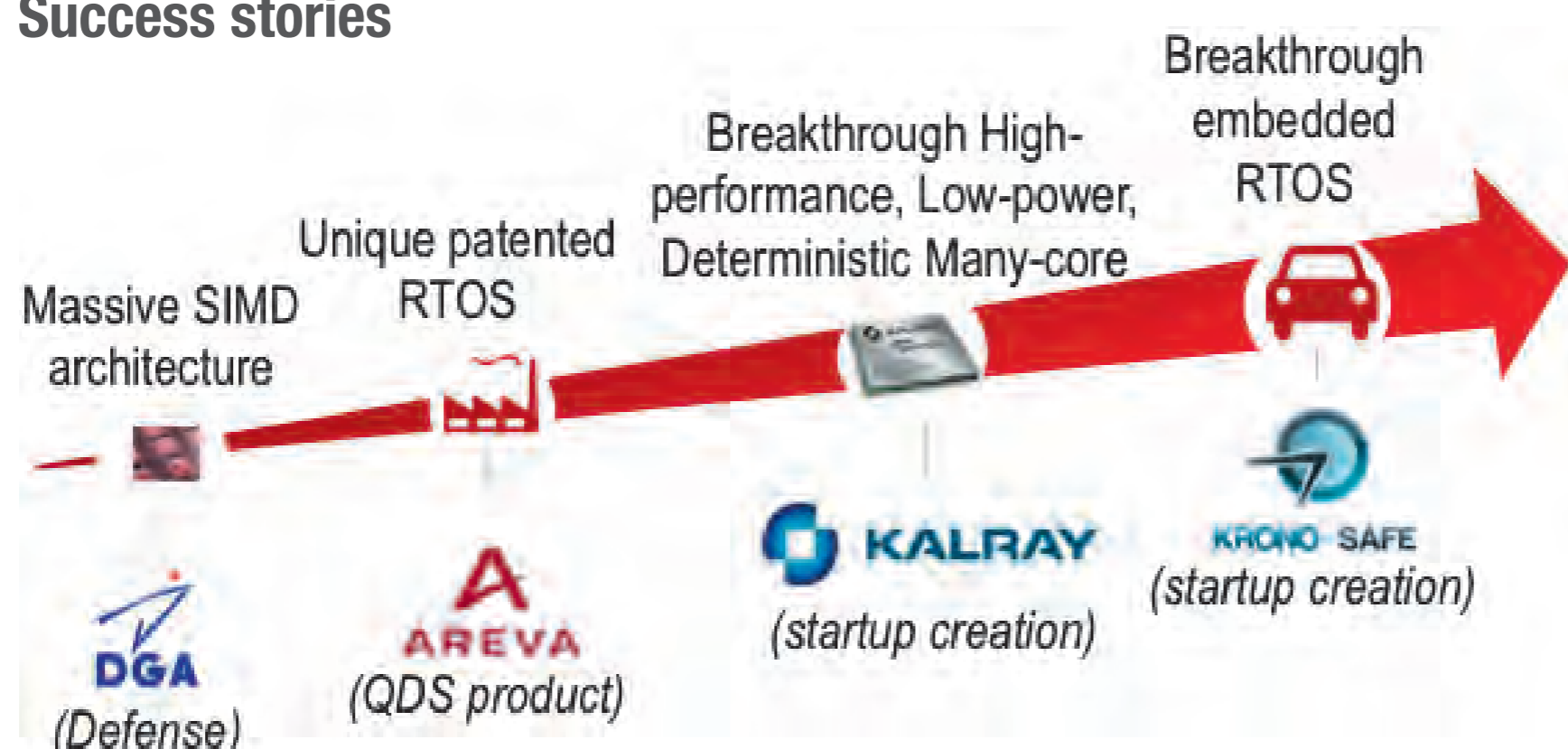


XCOEUR

Enabling **high-performance low-power many-core** CPU for CPS

20-year experience in computing and real-time

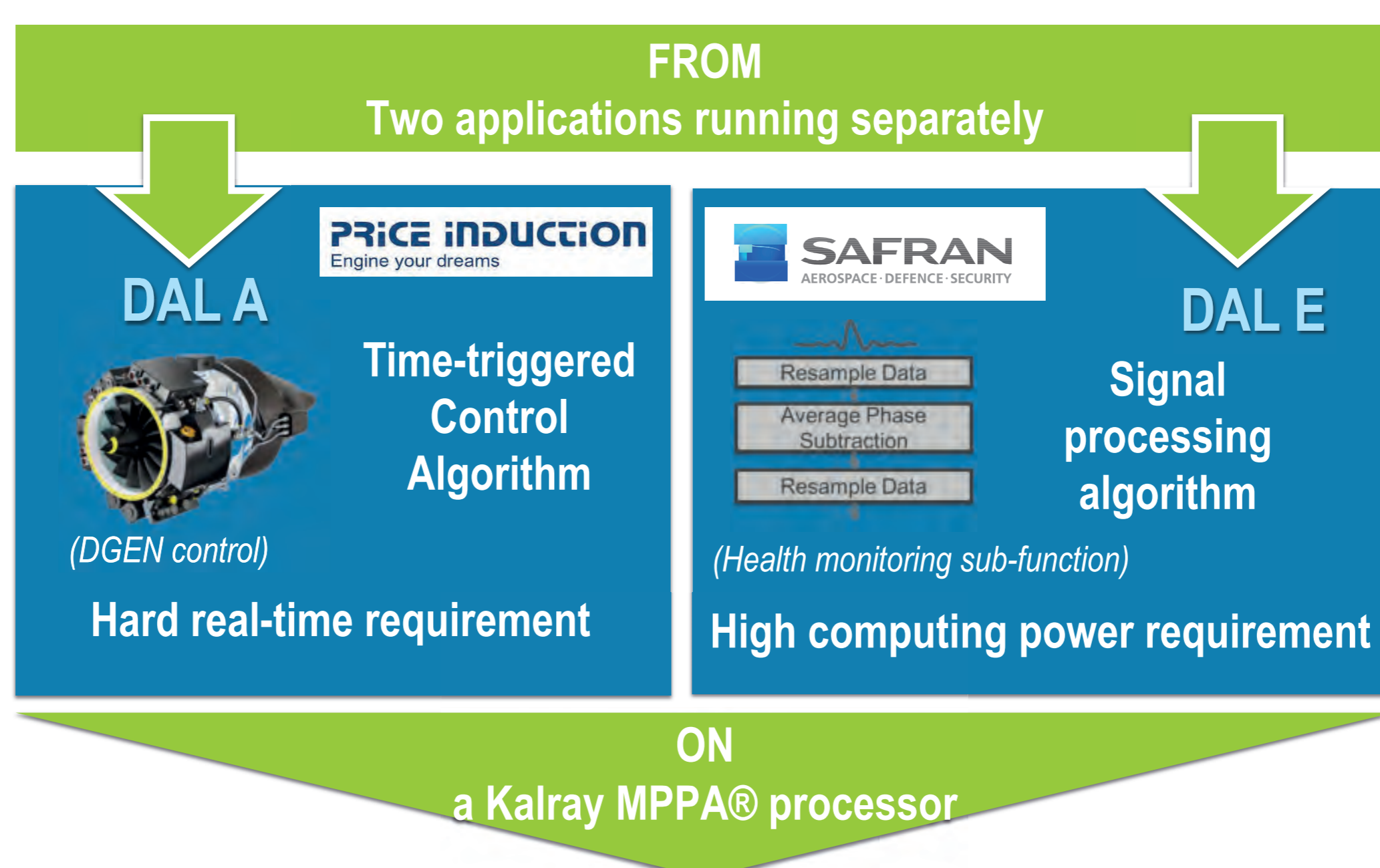
Success stories



- Some of our expertise**
- Real-time and Secured Hypervision
 - Cyber-Physical Systems design
 - Heterogeneous and homogeneous multicore
 - Neural network processor
 - Reconfigurable computing

Mixed-criticality avionics case study

To identify the Kalray MPPA® architecture configurations enabling a significant performance increase and better silicon usage in a Certification context



Non-interference in a cluster
Segregation and performance in a NoC



Signal processing execution speedup up to x35 (vs. single CPU)
Recommendations for architecture design refinement

Our know-how offer

HW & SW co-design for performance, real-time and low-power
CPS sizing, simulation and design optimization (CPS Impulse)

Contact

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