XCOEUR

Enabling high-performance low-power many-core CPU for CPS

20-year experience in computing and real-time

Some of our expertise
Real-time and Secure Hypervision
Cyber-Physical Systems design
Heterogeneous and homogeneous multicore
Neural network processor
Reconfigurable computing

Mixed-criticality avionics case study
To identify the Kalray MPPA® architecture configurations enabling a significant performance increase and better silicon usage in a Certification context

FROM
Two applications running separately

DAL A
(PIC18F2520)
Time-triggered Control Algorithm
Hard real-time requirement

ON
a Kalray MPPA® processor

DAL E
(PIC24F3243)
Signal processing algorithm
High computing power requirement

Our know-how offer
HW & SW co-design for performance, real-time and low-power CPS sizing, simulation and design optimization (CPS Impulse)

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